

ABSTRACT OF THE DISCLOSURE

A code processing circuit includes a plurality of coders which encode different kinds of data, respectively, a first buffer which stores the codes outputted from the coders provided corresponding to the plurality of coders, a second buffer which stores the lengths of the codes outputted from the coders provided corresponding to the plurality of coders, a first adder which adds the code lengths stored in the second buffer provided corresponding to the plurality of coders, a second adder which adds all the code lengths added in the first adder, and an adjustment unit which adjusts an output code by the unit of 1 bit based on the codes stored in the first buffer, the code lengths stored in the second buffer and the code lengths added in the second adder.